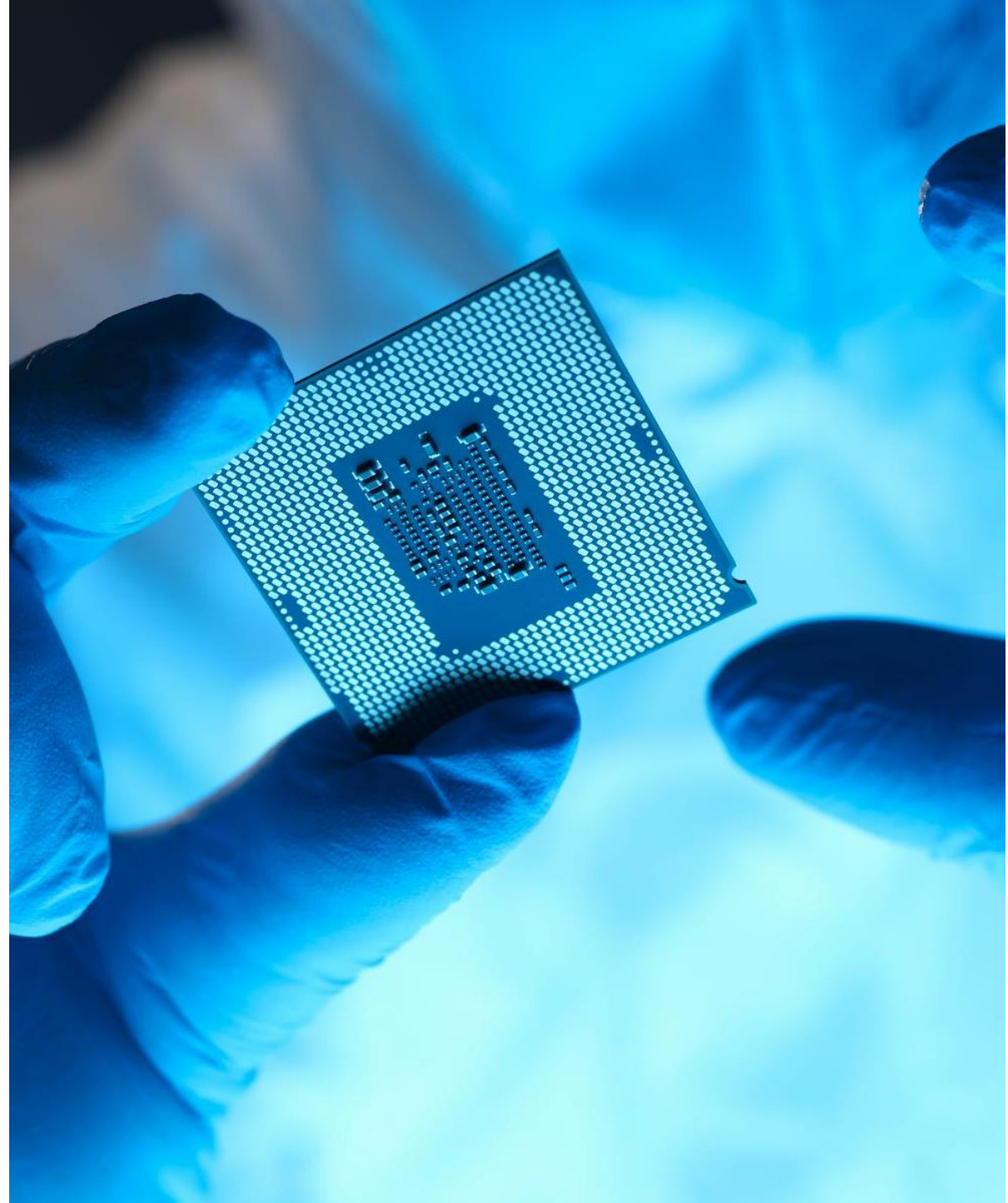


We are

ChipFlow

**Putting the future of innovation
in your hands**

September 2024





Operational start in 2023 (UK + EU + US), VC backed

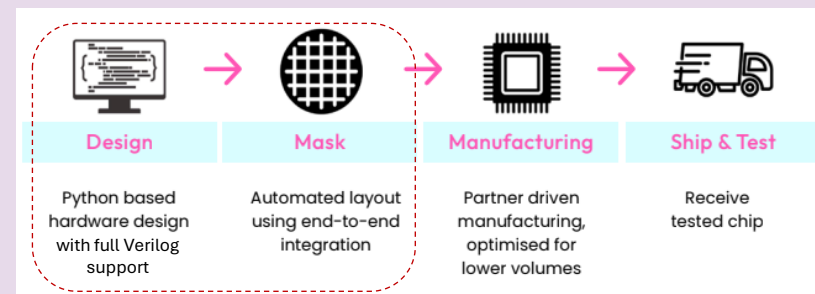
Current team 12 + 3 bringing together OSS HW, EDA tool & Semiconductor Supply Chain leaders

Want to lower the barrier for OEMs to innovate with ASICs

Target customers – Offering for OEMs with little or no ASIC experience in Automotive, CE, Industrial IoT

Offering – ‘one-stop-shop’ for custom ICs (ASICs)

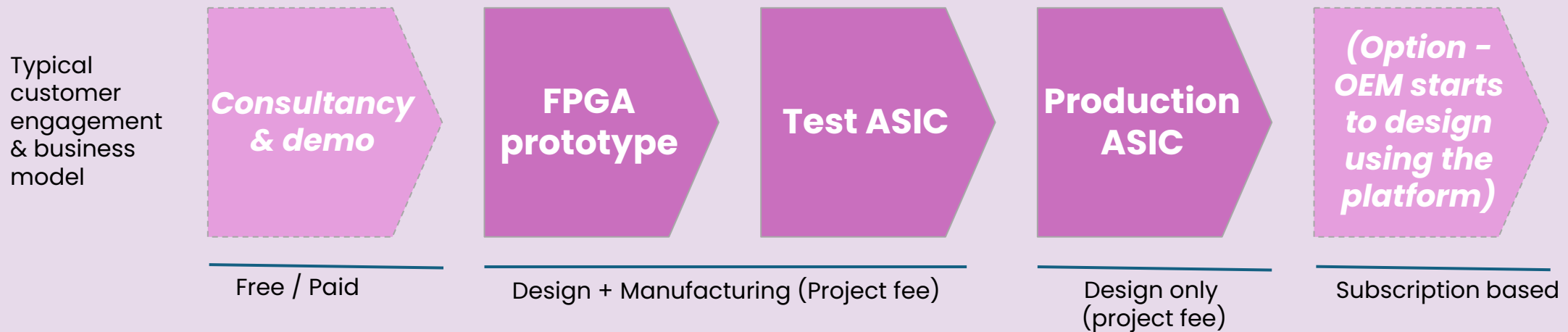
- ‘ASIC design house’
 - Services – Industry sector thought leadership + IC Design
 - Platform
- At the core is PaaS Design platform
 - Front-end & back-end -> GDSii file
 - Supply Chain by partners (PDKs integrated to the platform)



Platform

Innovate with ASICs comfortably without commitments until you are ready

- Fast prototyping – using FPGAs and / or ASICs
- Production volume ASICs



Focus on simplifying legacy node-based designs:

- Automotive Zonal architecture(s)
- CE / IoT: Risc-V designs

ChipFlow market traction

Phase 1 – Proof of Concept (2022–2023)

- Working with Pragmatic Semiconductor Ltd to prove the design flow with FlexICs
- Several successful tapeouts

Phase 2 – Make market ready (2023–>)

- Working with leading Automotive OEM and GlobalFoundries to prove design flow with Automotive Zonal Architecture ASICs
- Successful GF130 tapeout in April'24
- On-going project for a leading-edge Zonal architecture Remote I/O ASIC with aim towards Production

Phase 3 – Scale (2024–>)

- Bring in new customers and 2nd Supply Chain Partner
- Deploy ChipFlow platform to European Chips Act driven Design Platform as the main 'Innovation Flow' (Free to Use) targeted for EU SME OEMs

**Are you interested to
innovate with ASICs?**

**It is as simple as scheduling a free consultancy
& demo**